

20	RST	I	When RST="L", all control registers are re-initialized by their default states. Since UC1608x has built-in Power-ON-Reset and Software Reset command, RST pin is not required for proper chip operation. When RST is not used, connect the pin to VDD.																								
21	CS	I	Chip Select. The chip is selected when CS="H". When the chip is not selected, D[7:0] will be high impedance.																								
22	BM0	I	Bus mode: The interface bus mode is determined by BM[1:0] and D[7:6] by the following relationship: <table border="1" style="margin: 10px auto;"> <thead> <tr> <th>BM[1:0]</th> <th>D[7:6]</th> <th>Mode</th> </tr> </thead> <tbody> <tr> <td>11</td> <td>Data</td> <td>6800/8-bit</td> </tr> <tr> <td>10</td> <td>Data</td> <td>8080/8-bit</td> </tr> <tr> <td>01</td> <td>0X</td> <td>6800/4-bit</td> </tr> <tr> <td>00</td> <td>0X</td> <td>8080/4-bit</td> </tr> <tr> <td>01</td> <td>10</td> <td>3-wire SPI w/ 9-bit token (S9: conventional)</td> </tr> <tr> <td>00</td> <td>10</td> <td>4-wire SPI w/ 8-bit token (S8: conventional)</td> </tr> <tr> <td>00</td> <td>11</td> <td>3- or 4-wire SPI w/ 8-bit token (S8uc: Ultra-Compact)</td> </tr> </tbody> </table>	BM[1:0]	D[7:6]	Mode	11	Data	6800/8-bit	10	Data	8080/8-bit	01	0X	6800/4-bit	00	0X	8080/4-bit	01	10	3-wire SPI w/ 9-bit token (S9: conventional)	00	10	4-wire SPI w/ 8-bit token (S8: conventional)	00	11	3- or 4-wire SPI w/ 8-bit token (S8uc: Ultra-Compact)
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5. Outline Dimension & Block Diagram

